

This listing of claims replaces all prior versions, and listings of claims in the instant application:

**Listing of Claims:**

Cancel Claim 1 to Claim 19.

20. (Currently Amended) A hardware I/O control block memory array comprising:

a first hardware I/O control block having a sister hardware I/O control block field; and

a second hardware I/O control block having a sister hardware I/O control block field wherein said sister hardware I/O control block field of said first hardware I/O control block includes a pointer to said second hardware I/O control block and said sister hardware I/O control block field of said second hardware I/O control block includes a pointer to said first hardware I/O control block wherein said first and second hardware I/O control blocks are used for a mirrored data transaction.

Cancel Claim 21.

22. (New) The hardware I/O control block memory array as in Claim 20 wherein when the pointer in the sister field of the first hardware I/O command block is a valid pointer upon completion of execution of said first hardware I/O control block, the sister field of said second hardware I/O control block is changed to an invalid pointer.

23. (New) The hardware I/O control block memory array as in Claim 20 wherein when the pointer in the sister field of the second hardware I/O command block is a valid pointer upon completion of execution of said second hardware I/O

control block, the sister field of said first hardware I/O control block is changed to an invalid pointer.

24. (New) The hardware I/O control block memory array as in Claim 23 wherein said invalid pointer comprises a null identification number.

25. (New) The hardware I/O control block structure stored in a memory as in Claim 18 wherein said mirrored data transaction comprises a read transaction.

26. (New) The hardware I/O control block structure stored in a memory as in Claim 18 wherein said mirrored data transaction comprises a write transaction.

27. (New) A system comprising:

a host adapter;

a plurality of target devices coupled to said host adapter; and

a memory coupled to said host adapter, and said memory having stored therein a hardware I/O control block memory array comprising:

a first hardware I/O control block having a sister hardware I/O control block field; and

a second hardware I/O control block having a sister hardware I/O control block field wherein said sister hardware I/O control block field of said first hardware I/O control block includes a pointer to said second hardware I/O control block and said sister hardware I/O control block field of said second hardware I/O control block includes a pointer to said first hardware I/O control block wherein said first and second hardware I/O control blocks are used for a mirrored data transaction

using at least two target devices in said plurality of target devices.

28. (New) The system as in Claim 27 wherein when the pointer in the sister field of the first hardware I/O command block is a valid pointer upon completion of execution of said first hardware I/O control block, the sister field of said second hardware I/O control block is changed to an invalid pointer.

29. (New) The system as in Claim 27 wherein when the pointer in the sister field of the second hardware I/O command block is a valid pointer upon completion of execution of said second hardware I/O control block, the sister field of said first hardware I/O control block is changed to an invalid pointer.

30. (New) The system as in Claim 28 wherein said invalid pointer comprises a null identification number.

31. (New) The system as in Claim 29 wherein said invalid pointer comprises a null identification number.

32. (New) The system as in Claim 27 wherein said mirrored transaction comprises a read transaction.

33. (New) The system as in Claim 27 wherein said mirrored transaction comprises a write transaction.